



Commissioner for Patents  
Washington, DC 20231  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2897

<b>SERIAL NUMBER</b> 09/659,735	<b>FILING DATE</b> 09/11/2000 <b>RULE</b>	<b>CLASS</b> 46 103	<b>GROUP ART UNIT</b> 2123 2128	<b>ATTORNEY DOCKET NO.</b> NIT-83-02
------------------------------------	---	---------------------------	---------------------------------------	---

**APPLICANTS**  
Yasuhiko Sasaki, Koganei-shi, JAPAN;  
Kazuo Yano, Hino-shi, JAPAN;  
Shunzo Yamashita, Tokorozawa-shi, JAPAN;  
Koichi Seki, Hino-shi, JAPAN;

**\*\* CONTINUING DATA \*\*\*\*\***  
This application is a CON of 08/930,219 10/20/1997 PAT 6,260,185  
which is a 371 of PCT/JP96/01104 04/24/1996  
and is a CIP of 08/633,486 04/17/1996 PAT 5,712,792  
and is a CIP of 08/633,053 04/16/1996 PAT 5,923,189 *true Mj*

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***  
JAPAN 7-99204 04/25/1995  
JAPAN 7-96487 04/21/1995 *true Mj*

**IF REQUIRED, FOREIGN FILING LICENSE GRANTED**  
\*\* 11/03/2000

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 34	<b>TOTAL CLAIMS</b> 27	<b>INDEPENDENT CLAIMS</b> 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> 9/14/04 Initials				

**ADDRESS**  
24956

**TITLE**  
Method for designing semiconductor integrated circuit and automatic designing device

<b>FILING FEE RECEIVED</b> 816	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees ( Filing )
		<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
		<input type="checkbox"/> 1.18 Fees ( Issue )
		<input type="checkbox"/> Other _____
		<input type="checkbox"/> Credit

BEST AVAILABLE COPY